

## IN THE CLAIMS

Kindly cancel claim 3, without prejudice, and amend claims 1, 2, 6, 7 and 9 as shown in the following claim listing:

1. (currently amended) A quadrature device (1) comprising I and Q signal paths and corresponding signal paths components (2I, 2Q; 4I, 4Q; 9I, 9Q) showing a mismatch, characterized in that the quadrature device (1) comprises switching means (3, 3', 6, 3'') for exchanging the I and Q signals in said paths, and in that the quadrature device (1) is a sigma-delta A/D converter having I and Q feedback paths and D/A converters (9I, 9Q) in the feedback paths for exchanging I and Q feedback signals.

2. (currently amended) The quadrature device (1) according to claim 1, characterized in that the signal paths components (2I, 2Q; 4I, 4Q; 9I, 9Q) comprise amplifiers, ~~attenuators,~~ filters, mixers (4I, 4Q) and converters, ~~such as including~~ digital to analog (D/A) converters (9I, 9Q) or ~~an~~ analog to digital (A/D) converters (8I, 8Q) ~~and the like.~~

3. (cancel claim)

4. (previously presented) The quadrature device (1) according to claim 1, characterized in that the switching means (3, 3', 6, 3'')

are equipped for performing an I and Q data dependent exchange of the I and Q signals.

5. (previously presented) The quadrature device (1) according to claim 4, characterized in that the data dependent exchange takes place on an exclusive OR basis.

6. (currently amended) A communication device, receiver, transmitter, transceiver, telephone, mixer, modulator or demodulator, comprising a ~~quadrature~~-quadrature device (1) according to claim 1.

7. (currently amended) A method for reducing the effects of a mismatch between I and Q signal paths in a quadrature device (1), ~~characterized in that~~ comprising the steps of reducing the effects of said mismatch are reduced by exchanging of I and Q signals in said paths and providing the quadrature device (1) as a sigma-delta A/D converter having I and Q feedback paths and D/A converters (9I, 9Q in the feedback paths for exchanging I and Q feedback signals.

8. (original) The method according to claim 7, characterized in that the exchanging takes place with a switching frequency which exceeds the bandwidth of the I and Q signals.

9. (currently amended) The method according to claim 8, characterized in that the ~~quadrature~~quadrature device (1) is a sigma delta modulator producing I and Q bitstreams, and that I and Q feedback signals from said output bitstreams are exchanged.
10. (previously presented) The method according to claim 9, characterized in that the exchanging has a rate which is a multiple of the sampling frequency of said bitstreams.
11. (previously presented) The method according to claim 10, characterized in that the exchanging of the I and Q signals takes place in dependence on their I and Q data content.
12. (previously presented) The method according to claim 11, characterized in that the exchanging of the I and Q paths takes place on an exclusive OR basis, whereby alternately the I and Q signals are fed back as they are or are fed back interchanged in exclusive OR dependence on the I and Q data content.